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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Machala III, et al.

Art Unit:

1763

Serial No.:

10/735,229

Examiner:

Goudreau, G.

Filing Date:

12/11/2003

Docket No.:

TI-36674

Customer No.:

23494

Conf. No.:

1860

Title: METHOD FOR IMPROVING TRANSISTOR LEAKAGE CURRENT

UNIFORMITY

CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-872-9306 on the date shown below:

Jacquelipe J. Gamer, Reg. No. 36,144

May 2, 2005

Date

FACSIMILE COVER SHEET

X FACSIMILE COVER SHEET NEW APPLICATION DECLARATION (# Pages) ASSIGNMENT (# Pages) FORMAL DRAWINGS INFORMAL DRAWINGS CONTINUATION APP'N (# Pages) DIVISIONAL APP'N		X AMENDMENT (6 Pages) X EOT 3mth (2 Pages) NOTICE OF APPEAL (# Pages) APPEAL (# Pages) ISSUE FEE (# Pages) REPLY BRIEF (IN TRIPLICATE) (# Pages)		
NAME OF INVENTOR(S): Machala		Serial No.: 10/735,229		
TITLE OF INVENTION:		Filing Date: 12/11/2003		
METHOD FOR IMPROVING		Conf. No.: 1860	c)	
TI FILE NO.:	DEPOSIT ACCT. NO.:	1	- J	AFF
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FAXED: 5/2/2005 DUE: 2/1/2005 ATTY/SECY: JJG			GPE/JOUS	2005

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